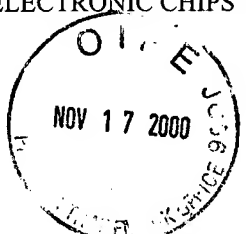


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul A. Farrar ✓
Title: PACKAGING OF ELECTRONIC CHIPS WITH AIR-BRIDGE STRUCTURES ✓
Docket No.: 303.603US1 ✓
Filed: August 25, 1999 ✓
Examiner: 
Serial No.: 09/382,929 ✓
Due Date: November 25, 2000
Group Art Unit: 2936
Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

- X A return postcard.
- X A Supplemental Information Disclosure Statement (1 pgs.), Form 1449 (1 pgs.), and copies of 6 cited references.

Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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By: Danny J. Padya
Atty: Danny J. Padya
Reg. No. 35,635

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 14 day of November, 2000.

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